Name	Mr.DEEPAK DATTATRAY PATIL					
Address	A/P- Mudhal, Tal- Bhudargad, Dist-Kolhapur. Maharastra					
Date of Birth	25-06-1986					
Designation	HOD ELECTRICAL ENGINEERING DEPARTMENT					
Date of Joining	17-Jun-15					
Mobile No.	9421718850					
E-Mail ID	dipu_patil2007@rediffmail.com					
Educational Qualifications		SSC	HSC	Diploma	U.G.	P.G.
	Degree	SSC	HSC	Diploma Electronics	BE Electronics	ME Electronics
	Specialisation	SSC	Science	Industrial Electronics	Electronics	Electronics
	Institution Name	P.B.Patil High School Mudhal	Murgud College Murgud	Govt.Polytechnic Kolhapur	TKIET Warananagar	TKIET Warananagar
	University/Board	Kolhapur	Kolhapur	Aotonomous	Shivaji University	Shivaji University
	Percentage	77.33%	50.55%	78.25%	65.81%	67.72%
	Remark	First Class Distinction	Second Class	First Class Distinction	First Class	First Class
	Year of Passing	2002	2004	2007	2010	2015
Experience Research Papers Published	Institution Name	Teaching		No.of years	Natur	re of work
		Sahyadri Polytechnic Sawarde		4.5	Lecturer	
		YBP Sawantwadi		7 yrs Till date	Head of Department	
		Total Years		11.6		
	 2.Paper Published titled "Remote Monitoring and Controlling of Industrial Parameters By Zigbee." in International Journal of Electrical and Electronics Engineering (IJEEE), Volume- 07, Issue 02, July, 2015 3.Paper Published titled "Design of Remote Data Acquisition System Based on 3G." in International Journal of Latest Technology in Engineering, Management & Applied Science (IJLTMAS), Volume- IV, Issue IX, Sept.2015 					
Conference attended :	Presenting Paper on "Remote Monitoring and Controlling of Industrial Parameters By Zigbee." In 2nd International Conference on Science, Technology and Management held on 27th Sept.2015 at Delhi University, Conference Center, and New Delhi.					
Training attended :	1.One week FDP on "Adavnced Trends in Embedded System" organized by Bharati Vidyapeeth Institute of Technology, Navi Munbai from 07 march to 11 march 2022. 2.One week FDP on "Recent Advance and real time applications in Artificial Intelligence" organized by Vaagdevi college of Engineering from 25 October to 29 October 2021 3. One week "Image And Embedded Processing" organized by NITTTR Chandigarh on 10 May 2020 to 15 May 2020 4.One week FDP on "Latex" organized by Sanjay Ghodawat University, Kolhapur on 27 April 2020 to 2 May 2020 5.4 weeks FDP on "Tools for online teaching Learning Process" organized by Government Polytechnic, Jalgaon from 20 April 2020 to 17 May 2020 6.One week "Moodle Learning and Management system" organized by Vivekanand College, Kolhapur from 25 April 2020 to 30 April 2020					
MSBTE/ Institute Duties performed:	1. Worked as IQAC Co-ordinator of Institute 2. Worked as Program Co-ordinatoe of AICTE-ISTE Faculty Development Program Organised by Yashwantrao Bhonsale Polytechnic, Sawantwadi 3. Member of External Institute Monitoring Committee(EIMC) 4. Chairman of External vigilance Squad of MSBTE Board exam, MSCIT Exam. 5. MSBTE Paper setter Moderator as well as Paper Setter (set more than 30 question papers of C,E and I Schemes) 6. Exam Controller of MSBTE board Exam. 7. Exam cell In charge of Exam cell of Institute 8. Guest speaker for career on wheel fair and school connect programm					

I hereby inform that the above mentioned information is true to the best of my knowledge.

Date:

Place:Mudhal